

**Manav Rachna University**  
**Estd. vide Haryana Act No. 26 of 2014**  
**(Formerly Manav Rachna College of Engineering)**  
**Sector-43, Aravali Hills, Faridabad**

**F.NO MRU/BoS/2015**

**Dated: 15.06.2021**

**OFFICE ORDER**

The Competent Authority is pleased to re-constitute the Board of Studies of the Departments of ECE as per the provisions contained in the Clause 21 of the MRU (First) Statute, the composition of which will be as under:

**Department of Electronics and Communication Engineering**

- |   |                        |
|---|------------------------|
| 1. HoD  | Chairperson            |
| 2. Dr Charu pathak, Assoc. Prof.,                                 | Member                 |
| 3. Dr. Yogita Gupta, Asst. Prof                                   | Member                 |
| 4. Dr. Meenakshi Gupta, Asst. Prof                                | Member                 |
| 5. Dr. K. Deepa, Asst. Prof.,                                     | Member                 |
| 6. Mr.Prateek Sikka- Director,<br>Sahai Research Labs             | Industry Expert        |
| 7. Prof. Vijay Janyani, Professor, MNIT Jaipur                    | External Expert Member |
| 8. Mr. Mohit Yadav, Design Engineer Alumni<br>TRUE Chip Solutions | Special Invitee        |

  
(Dr. K. Singh)  
Registrar

**F.NO MRU/BoS/2015**

**Dated: 15.06.2021**

1. PS to VC for kind information to Hon'ble Vice Chancellor.
2. Chairperson and Members of the Committee
3. All the Deans and HODs for kind information
4. Mr. Prateek Sikka- Director, Sahai Research Labs, 2nd Floor, Bhagwan Sahai Palace, Metro Station Road, Sector 15, Noida, Uttar Pradesh 201301
5. Dr. Vijay Janyani, Professor, Dept. of Electronics & Comm. Engineering, MNIT Jaipur-302017
6. Mr. Mohit Yadav, Design Engineer TRUE Chip Solutions, Alumni
7. Registrar, MREI
8. DR (Admin) & Dy. CoE
9. CF & AO / GM (Purchase, Audit, Inventory)
10. Dy. Librarian

  
Registrar